

S/N 08/984,563

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al.

Examiner: Unknown

Serial No.: 08/984,563

Group Art Unit: Unknown

Filed: December 3, 1997

Docket: 303.623US4

Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

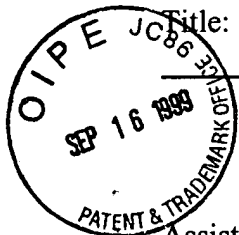
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Assistant Commissioner for Patents

Washington, D.C. 20231

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05, 402.07, and 324
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CERTIFICATE UNDER 37 CFR §3.73(b)

Micron Technology, Inc. hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventors filed May 20, 1996 and recorded on Reel 8024, Frames 0448-0452 in the parent application assigned U.S. Serial No. 08/650,719. To the best of my knowledge and belief, title is in Micron Technology, Inc., the assignee.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Michael L. Lynch, am empowered to sign this certificate on behalf of Micron Technology, Inc., the assignee.

I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

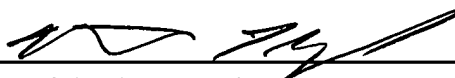
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Oct 17, 1997

By



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